

**ABSTRACT OF THE DISCLOSURE**

An apparatus for testing an integrated circuit chip includes a printed circuit device having connector pads, contacts, and traces extending between at least some of the connector pads and the contacts. The printed circuit device has openings therethrough, intersecting the contacts, that are adapted to receive the pins extending from the integrated circuit chip so that the contacts may electrically contact the pins extending from the integrated circuit chip. The apparatus further includes a connector electrically interconnected with at least some of the connector pads. The apparatus is adapted to be disposed between the integrated circuit chip and a chip socket, such that the pins extending from the integrated circuit chip may be inserted through the printed circuit device and into the chip socket.

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